L	Hits	Search Text	DB	Time stamp
Number	Hitts	Jealon Text		-
	1	4368514.pn.	USPAT;	2004/06/02
			US-PGPUB	10:18
_	270	(first or second) adj1 (processor or cpu)	USPAT; US-PGPUB	2004/05/26 16:08
	4	with (shar\$3 near3 (RAM or memory)) ((first or second) adj1 (processor or	USPAT;	2004/05/26
_	4	cpu) with (shar\$3 near3 (RAM or memory)))	US-PGPUB	16:07
		with clock\$1	02 20202	
_	37	((first) adj1 (processor or cpu) with	USPAT;	2004/05/27
		(second) adj1 (processor or cpu)) with	US-PGPUB	15:23
		synchronous\$4		
	2	(((first) adj1 (processor or cpu) with	USPAT;	2004/05/26
		(second) adjl (processor or cpu)) with	US-PGPUB	16:16
		(shar\$3 near3 (RAM or memory))) and (((first) adj1 (processor or cpu) with		
		(second) adj1 (processor or cpu)) with		
		synchronous\$4)		
_	43	(clock near3 generat\$3) with	USPAT;	2004/05/26
		(multiprocessor)	US-PGPUB	16:16
-	50	(clock near3 generat\$3) with	USPAT;	2004/05/28
		(multiprocessor or multi-processor)	US-PGPUB	18:06
-	50	((first) adj1 (processor or cpu) with (second) adj1 (processor or cpu)) with	USPAT; US-PGPUB	2004/05/27
		(second) adji (processor or cpu)) with   phase	05 19100	11.55
_	45	((first) adj1 (processor or cpu) with	USPAT;	2004/05/27
		(second) adj1 (processor or cpu)) with	US-PGPUB	15:46
		(RAM or memory) with clock\$1		
-	513	(memory adj1 array) with (memory adj1	USPAT;	2004/05/27
		controller)	US-PGPUB	15:52
-	123	((first) adj1 (processor or cpu) with	USPAT; US-PGPUB	2004/05/27 16:16
		(second) adj1 (processor or cpu)) with (shar\$3 near3 (RAM or memory))	US-PGPUB	16:16
_	1	((memory adj1 array) with (memory adj1	USPAT;	2004/05/27
		controller)) same (multi-processor or	US-PGPUB	16:17
		(multi adj1 processor))		
-	0	((memory adj1 array) with (memory adj1	USPAT;	2004/05/27
		controller)) and (((first) adj1	US-PGPUB	15:52
		(processor or cpu) with (second) adj1		
		(processor or cpu)) with (shar\$3 near3 (RAM or memory)))		
_	1038	(memory adj1 array) same (memory adj1	USPAT;	2004/05/27
		controller)	US-PGPUB	16:03
_	2	((memory adj1 array) same (memory adj1	USPAT;	2004/05/27
		controller)) and (((first) adj1	US-PGPUB	15:52
·		(processor or cpu) with (second) adj1		
		(processor or cpu)) with (shar\$3 near3 (RAM or memory)))		
_	581	(memory adj1 array) same (memory adj1	USPAT;	2004/05/27
	]	controller) same address\$2	US-PGPUB	16:03
-	14	((memory adj1 array) same (memory adj1	USPAT;	2004/05/27
		controller) same address\$2) and ((first)	US-PGPUB	16:03
		adj1 (processor or cpu) with (second)		
		adj1 (processor or cpu))	USPAT;	2004/05/27
_	3	((first) adj1 (processor or cpu) with (second) adj1 (processor or cpu)) with	USPAT; US-PGPUB	16:18
		(disabl\$3 near3 clock)	05 13105	-
_	1	(disabl\$3 near3 clock) same	USPAT;	2004/05/27
		(multi-processor or (multi adj1	US-PGPUB	16:18
		processor))		
-	9	(disabl\$3 near3 clock) with ((first or	USPAT;	2004/05/27
		second) adj1 (processor or cpu))	US-PGPUB	16:31 2004/05/27
-	14	((disabl\$3) with ((first or second) adj1 (processor or cpu))) with clock\$1	USPAT; US-PGPUB	16:32
_	140	(disabl\$3) with ((first or second) adj1	USPAT;	2004/05/27
	140	(processor or cpu))	US-PGPUB	16:40
-	1937	713/32\$.ccls.	USPAT;	2004/05/27
			US-PGPUB	16:40
-	37	713/32\$.ccls. and ((first adj1 (processor	USPAT;	2004/05/27
,		or cpu)) with (second adj1 (processor or	US-PGPUB	16:54
L		cpu)))	<u>L</u>	1

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-	54	713/32\$.ccls. and ((first adj1 (processor or cpu)) same (second adj1 (processor or	USPAT; US-PGPUB	2004/05/27 16:55
-	16	cpu))) (713/32\$.ccls. and ((first adj1 (processor or cpu)) same (second adj1	USPAT; US-PGPUB	2004/05/27 17:00
		(processor or cpu)))) and (stop\$3 near2 clock\$1)		
-	8307	multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1)	USPAT; US-PGPUB	2004/05/27 17:10
-	783	(multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1)) and (control\$3 near2	USPAT; US-PGPUB	2004/05/27 17:08
_	302	clock\$1) (multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1)) and ((stop\$3 or	USPAT; US-PGPUB	2004/05/27 17:05
-	202	disabl\$3 or inhibit\$3) near2 clock\$1) ((multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1)) and (control\$3 near2 clock\$1)) and ((multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1)) and ((stop\$3 or disabl\$3 or inhibit\$3) near2 clock\$1))	USPAT; US-PGPUB	2004/05/27 17:04
_	98	(multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1) and ((stop\$3 or disabl\$3 or inhibit\$3) near2 (clock\$1 near2 (output\$1 or signal\$1)))	USPAT; US-PGPUB	2004/05/27 17:05
_	23	<pre>(multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1)) and ((stop\$3 or disabl\$3 or inhibit\$3) near2 (clock\$1 near2 (output\$1)))</pre>	USPAT; US-PGPUB	2004/05/27 17:05
_	817	<pre>(multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1)) and (generat\$3 near2 clock\$1)</pre>	USPAT; US-PGPUB	2004/05/27 17:08
_	111	(multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1)) and (selective\$3 near2 disabl\$3)	USPAT; US-PGPUB	2004/05/27
	130	(multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1)) and (selective\$3 near2	USPAT; US-PGPUB	2004/05/27 17:12
-	42	<pre>(disabl\$3 or inhibit\$3 or stop\$3)) (multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1)) and ((selective\$3</pre>	USPAT; US-PGPUB	2004/05/27 17:15
-	25	near2 (disabl\$3 or inhibit\$3 or stop\$3)) with (processor\$1 or cpu\$1)) (multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1)) and ((selective\$3 near2 (disabl\$3 or inhibit\$3 or stop\$3))	USPAT; US-PGPUB	2004/05/28
_	21	<pre>with (clock\$1)) suspend\$3 near3 (second adj1 processor)</pre>	USPAT; US-PGPUB	2004/05/28
_	3227	external near3 reset	USPAT; US-PGPUB	2004/05/28 18:06
_	797 1113	external near3 reset adj1 signal\$1 external near3 (reset adj1 signal\$1)	USPAT; US-PGPUB USPAT;	2004/05/28 18:06 2004/05/28
_	13	(external near3 (reset adj1 signal\$1))	US-PGPUB USPAT;	18:10 2004/05/28
		and (multi-processor or (multi adj1 processor) or dual-processor\$2 or (dual adj1 processor\$1))	US-PGPUB	18:10
L	1	1		1

		I		<del>,</del>
· -	0	(external near3 (reset adj1 signal\$1)	EPO; JPO;	2004/05/28
		and (multi-processor or (multi adj1	IBM_TDB	18:10
		processor) or dual-processor\$2 or (dual	_	1
		adj1 processor\$1))		
-	185	external near3 (reset adj1 signal\$1)	EPO; JPO;	2004/05/28
			IBM TDB	18:10
_	0	first adj1 (external near3 (reset adj1	EPO; JPO;	2004/05/28
		signal\$1))	IBM TDB	18:10
_	17	first adj1 (external near3 (reset adj1	USPAT;	2004/05/28
		signal\$1))	US-PGPUB	18:25
_	207	(high adj2 (processor or cpu)) with (low	USPAT;	2004/05/28
		adj2 (processor or cpu))	US-PGPUB	18:26
-	9	((high adj2 (processor or cpu)) with (low	USPAT;	2004/05/28
		adj2 (processor or cpu))) and	US-PGPUB	18:49
		713/32\$.ccls.		
_	3	6240521.URPN.	USPAT	2004/05/28
				18:29
_	13	(first or second) adj1 external adj1	USPAT;	2004/05/28
		reset	US-PGPUB	18:49
_	1	"973888"	US-PGPUB	2004/06/02
				13:38
_	1	5903503.pn.	USPAT	2004/06/02
				10:27